

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicants: Shashi Sakhuja & Ilie Marian Poenaru

Assignee: Catalyst Semiconductor, Inc.

Title: Programmable Analog Bias Circuits Using Floating  
Gate CMOS Technology

Serial No.: 10/656,982

Filed: September 5, 2003

Examiner: Terry Lee Englund

Group Art Unit: 2816

Attorney Docket No.: CAT-00001

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Date: May 31, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Sir:

Applicants submit the following amendments and remarks in response to the Office Action mailed on February 10, 2005. A petition for extension of time, extending the period for response to June 10, 2005, accompanies the present Amendment.